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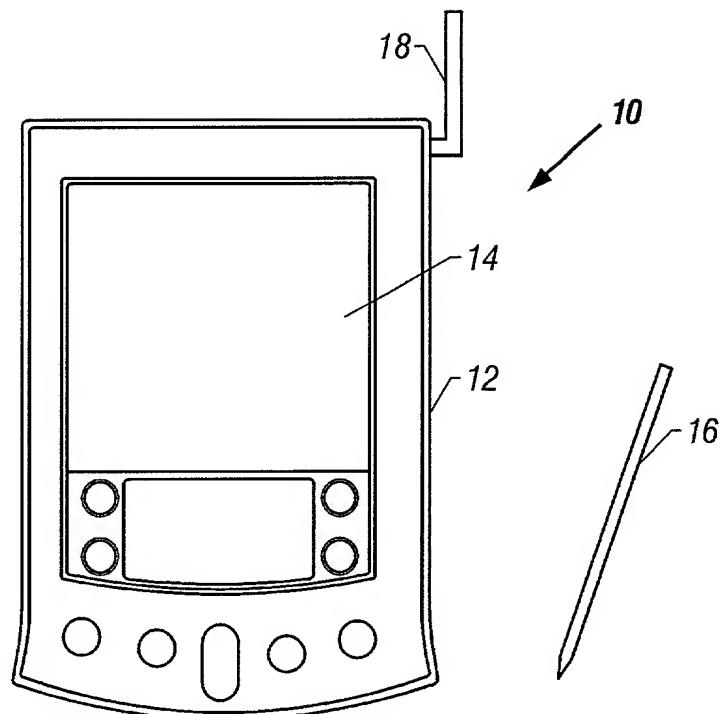


FIG. 1
(Prior Art)

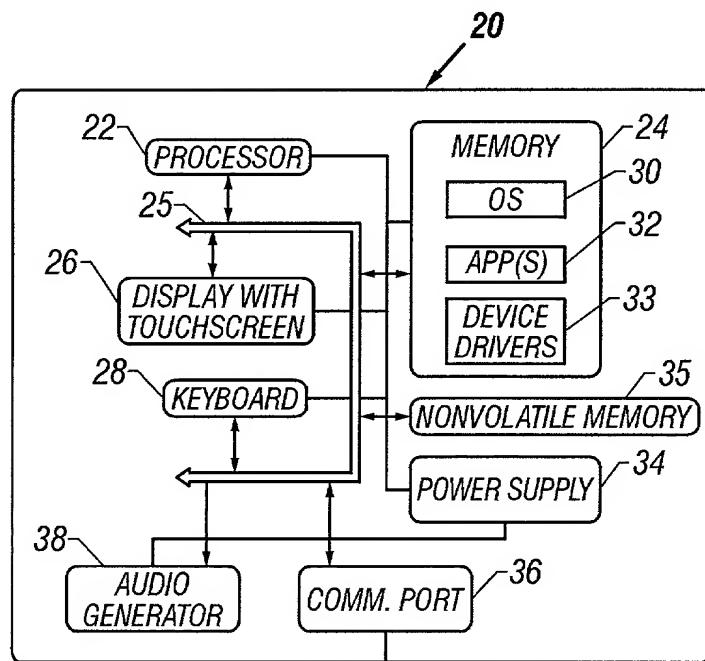


FIG. 2
(Prior Art)

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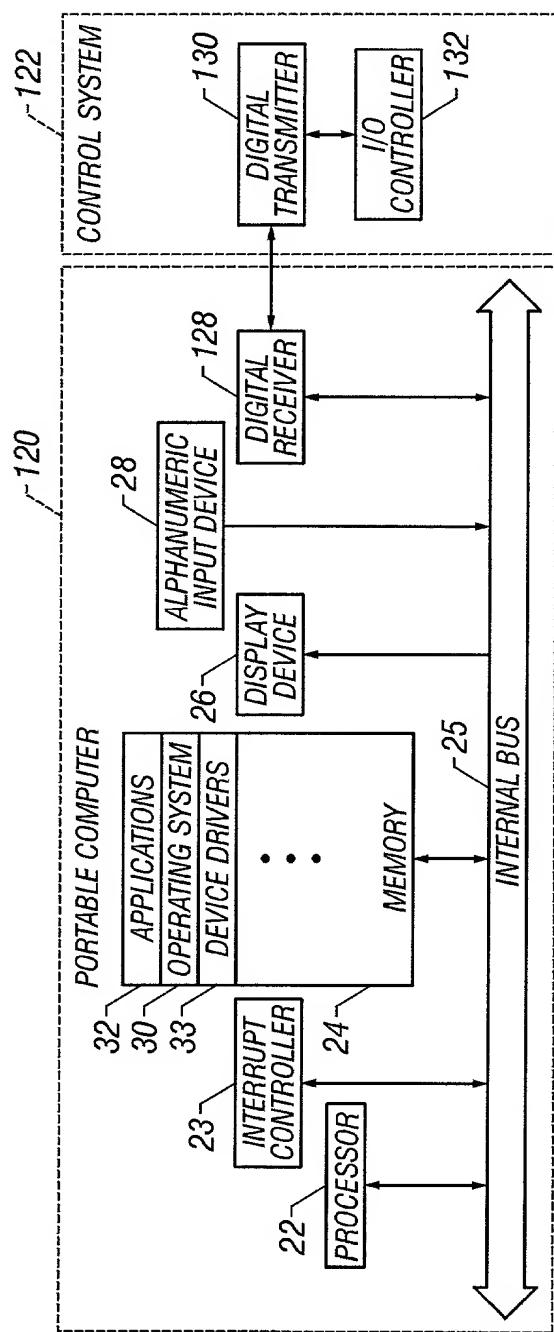


FIG. 3

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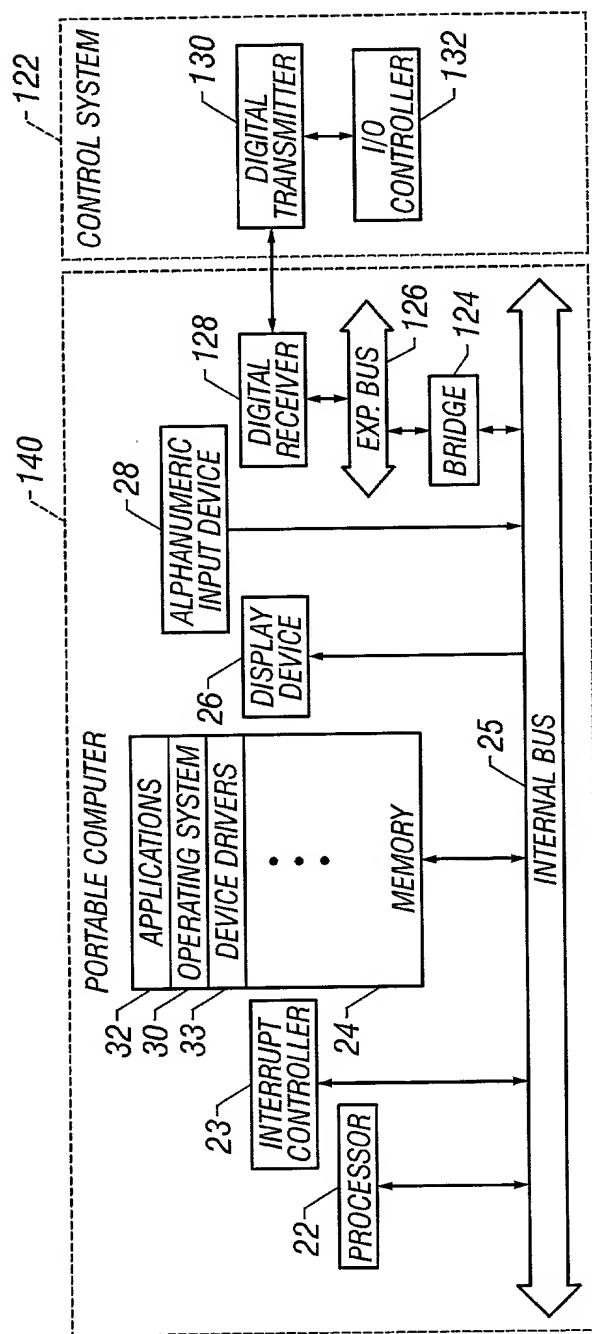


FIG. 4

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RECEIVER INTERFACE BUS

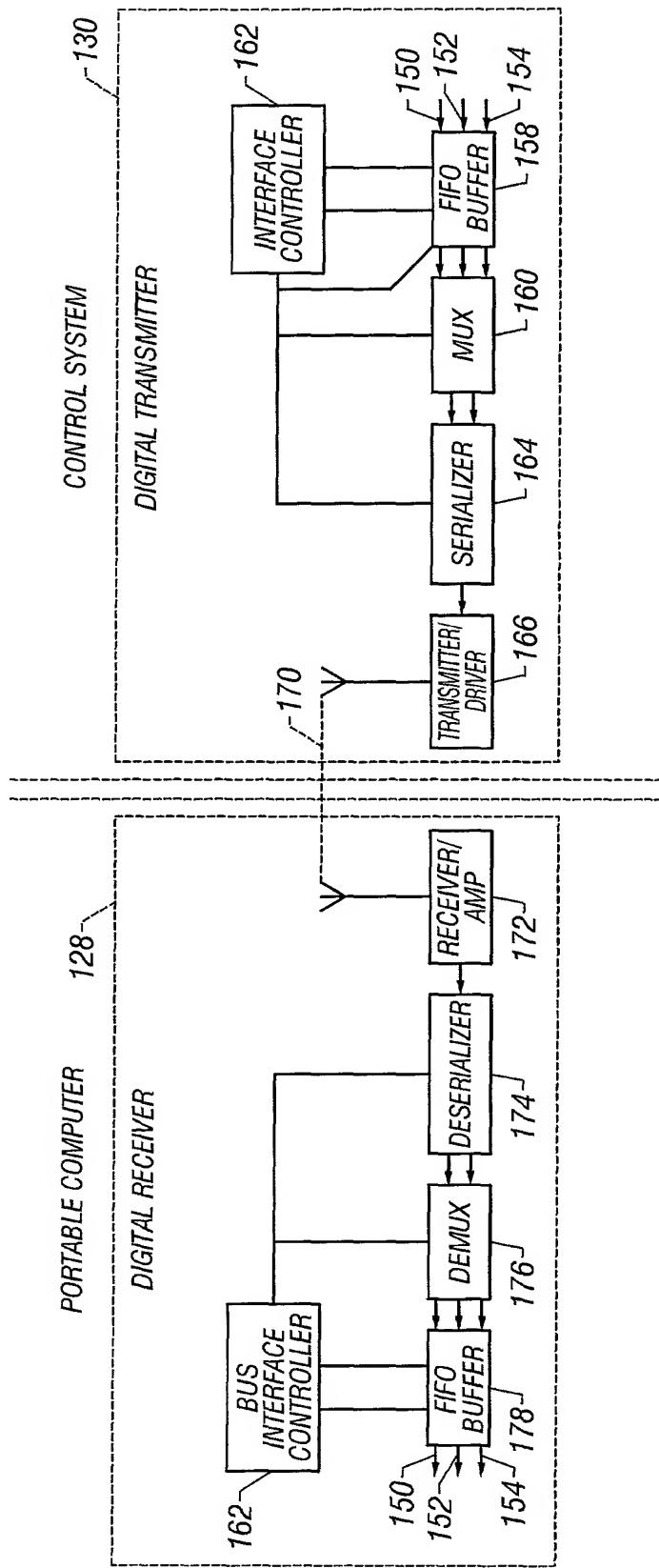


FIG. 5

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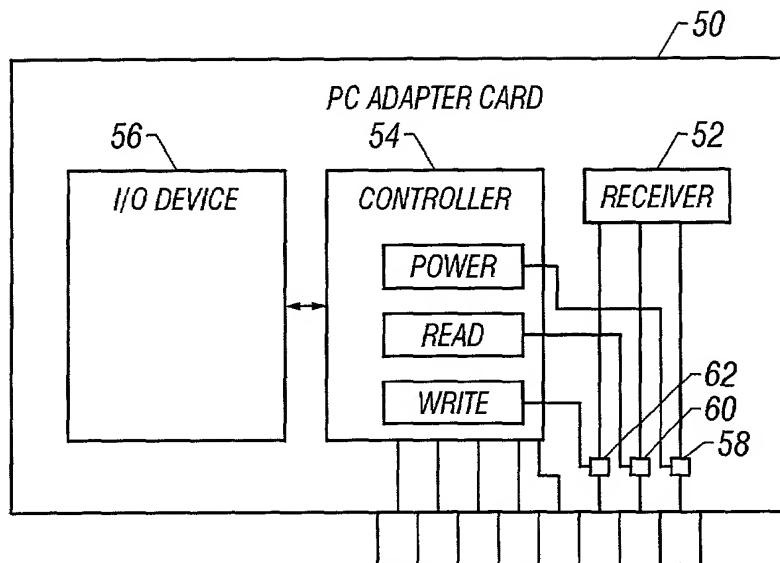


FIG. 6

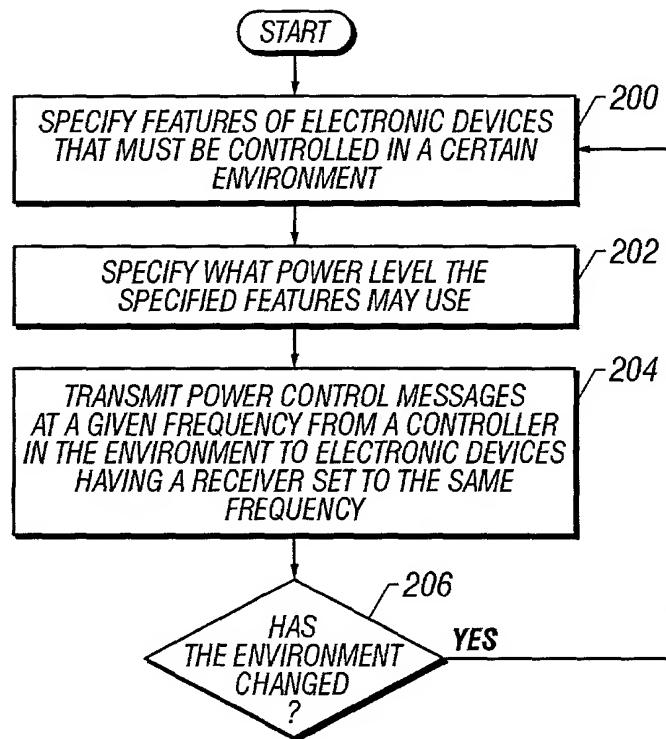
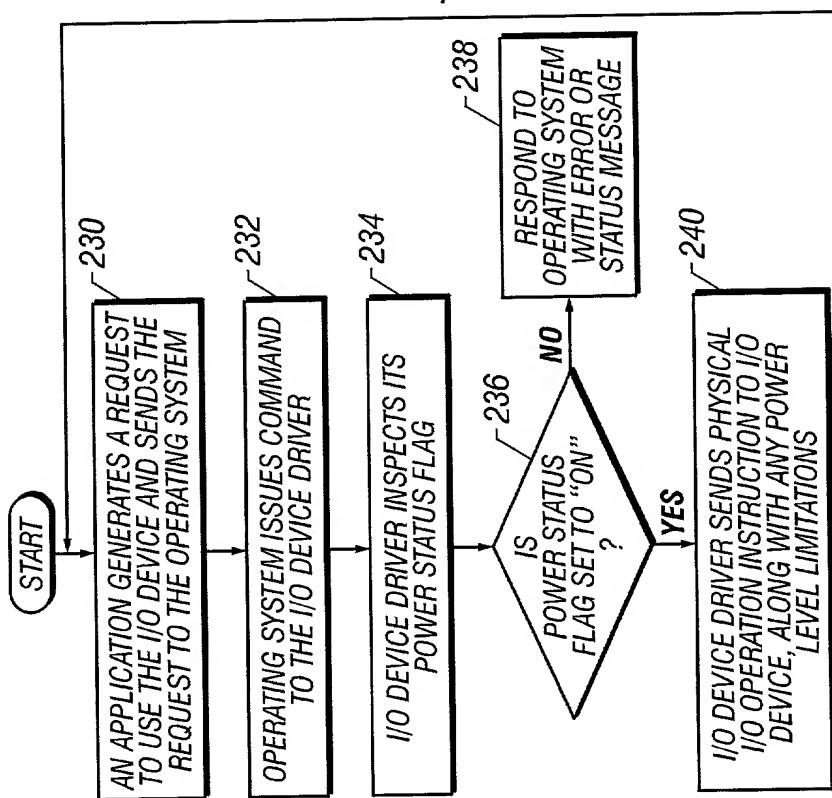
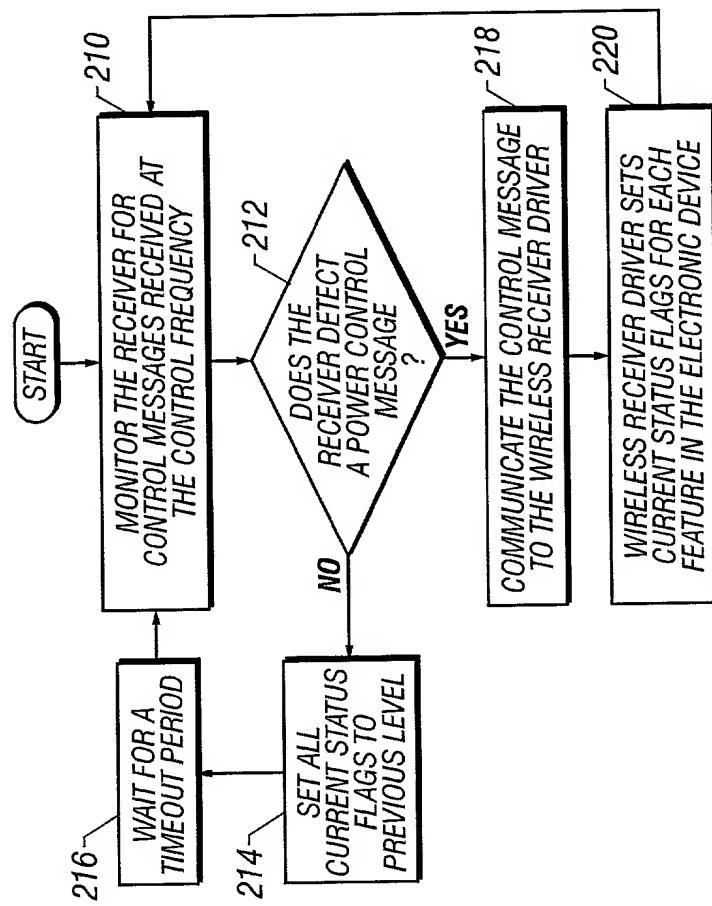


FIG. 7

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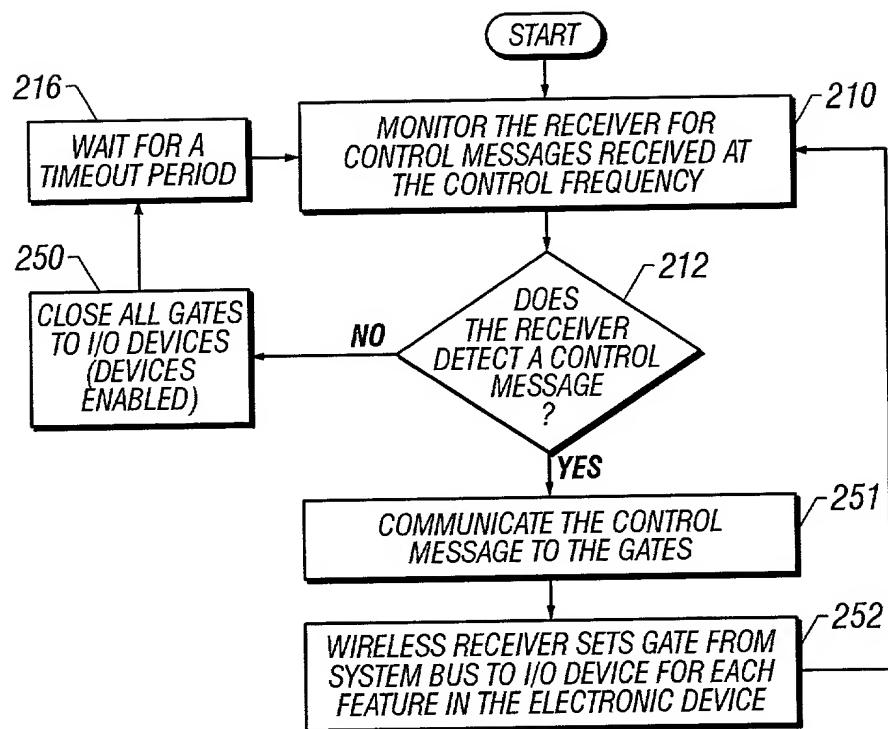


FIG. 10

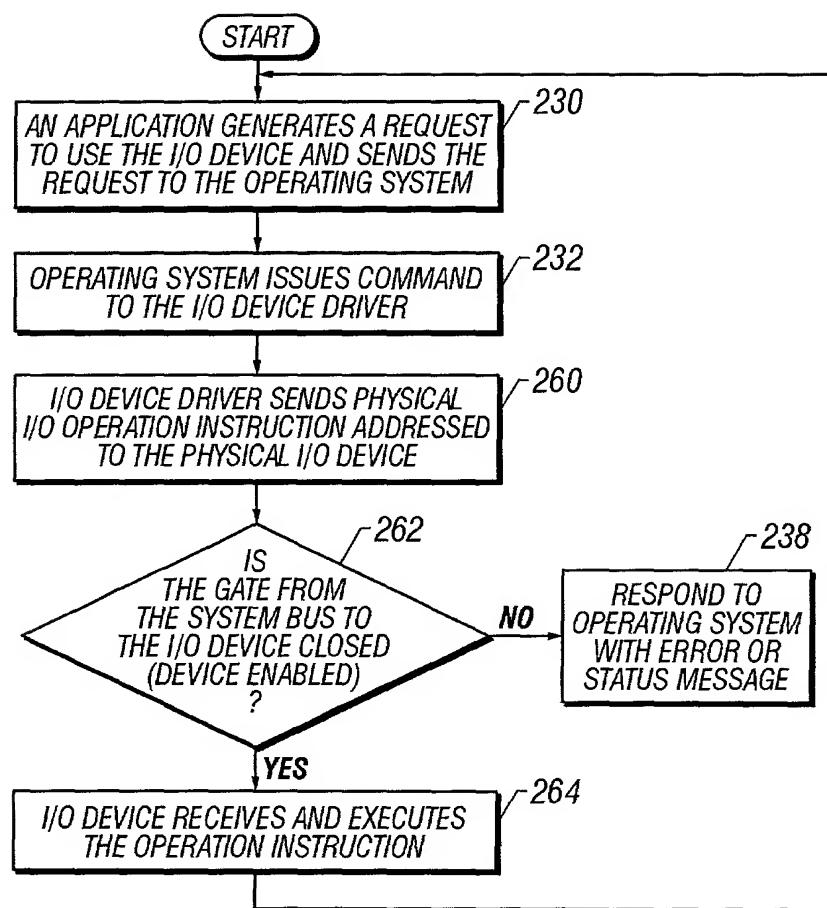


FIG. 11